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- (71) Applicant (for DE only): PHILIPS INTELLECTUAL PROPERTY & STANDARDS GMBH [DE/DE]; Steindamm 94, 20099 Hamburg (DE).
- (71) Applicant (for all designated States except DE, US):
 KONINKLIJKE PHILIPS ELECTRONICS N.V.
 [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven
 (NL).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): WAGNER, Martin [DE/DE]; c/o Philips Intellectual Property & Standards

GmbH, Weisshausstr. 2, 52066 Aachen (DE). MUTH, Matthias [DE/DE]; c/o Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE).

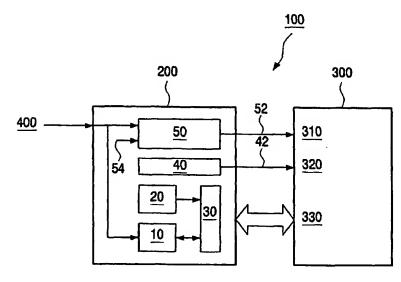
- (74) Agent: MEYER, Michael; Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE).
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(54) Title: METHOD AND BASE CHIP FOR MONITORING THE OPERATION OF A MICROCONTROLLER UNIT



(57) Abstract: To enable a method and a base chip (200) for monitoring the operation of at least one microcontroller unit (300) that is intended for at least one application and is associated with a system (100) to be further developed in such a way that the operation of the microcontroller unit (300), which is associated with a serial system, is monitored in such a way as to enable faulty operation, and particularly faulty operation that discharges the battery, to be reliably prevented, it is proposed that the microcontroller unit (300) has at least one non-volatile memory area (10) associated with it, the memory area (10) can be read from and/or written to by the microcontroller unit (300), and at least one set of statistics, and in particular a set of fault statistics, relating to the operation of the microcontroller unit (300), can be kept by means of the memory area (10).

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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MAT IPC 7 G06F1/32

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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols) IPC 7-606F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data

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Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswljk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016	Authorized officer Lanchès, P

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